

Applicants : Sydir et al.
Serial No. : 10/749,913
Filed : December 29, 2003
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Attorney's Docket No.: INTEL-013PUS
Intel Docket No. P17940

AMENDMENTS TO THE DRAWINGS:

The attached replacement sheets of drawings include changes to FIGS. 1 and 2 and replace the original sheets including FIGS. 1 and 2.

In Figure 1, reference 106a for the transmit buffer was changed to "106b"

In Figure 2, DES was amended to 3DES.

Attachments following last page of this Amendment:

Replacement Sheets (2 pages)

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REMARKS

Claims 1 to 34 are pending in this application of which claims 1, 10, 18, 25 and 32 are the independent claims. Favorable reconsideration and further examination are respectfully requested.

Applicants held a teleconference with the Examiner and the Examiner's Supervisor on Thursday, March 8, 2007 to clarify the 35 U.S.C. § 101 rejection of claims 10 to 14, 16 and 17. The Examiner indicated in the teleconference that independent claim 10 was rejected because the "authenticating" element did not produce a meaningful result. Applicants have removed the authenticating element in claim 10 and amended claim 10 to correspond to claim 1. The Examiner indicated that the foregoing amendments to claim 10 would overcome the §101 rejection. Applicants respectfully request withdrawal of the § 101 rejection of claims 10 to 14, 16 and 17

The Examiner objected to the drawings. Based on the foregoing amendment to the drawings, Applicants respectfully request withdrawal of the drawing objections.

The Examiner objected to the specification. Based on the foregoing amendment to the specification, Applicants respectfully request withdrawal of the specification objections.

Claims 1 to 6 and 8 to 29 were rejected under 35 U.S.C. § 102(b) as being anticipated by Ohta et al. (U.S. Patent Application Publication Number 2002/0083317 hereinafter "Ohta").

Claim 7 was rejected under 35 U.S.C. § 103(a) as being obvious over Ohta in view of Corder et al. (U.S. Patent Number 7,069,447)

Claim 1 is directed to a processor. The processor includes a crypto unit including a cipher core configured to cipher data received and authentication cores configured to authenticate the ciphered data. At least two authentication cores each implements a different authentication algorithm. The processor also includes an authentication buffer configured to store the ciphered data and provide the ciphered data to the authentication cores each in an amount based on the corresponding authentication algorithm implemented.

The applied art is not understood to disclose or to suggest the foregoing features of claim 1. In particular, Ohta does not disclose or suggest that the processor includes an authentication buffer configured to provide the ciphered data to the authentication cores each in an amount based on the corresponding authentication algorithm implemented.

Ohta discloses a data block accumulation unit 103 connected to a single encryption unit 102 and a single authentication processing unit 104 (see FIG. 2 of Ohta). As understood by Applicants, Ohta does not disclose an authentication buffer providing ciphered data to more than one authentication core. Moreover, the authentication unit 104 performs only one authentication algorithm (see paragraph [0056] of Ohta). The Examiner has indicated that Ohta teaches plural cipher processing units (see page 4 of the office action). Applicants respectfully submit that Ohta expressly recites "the number of at least one of the encryption processing unit and the authentication unit is two or more, and the number of the data block accumulation unit is equal to

that of the encryption unit (see paragraph [0012] of Ohta). If additional authentication algorithm processing is required additional accumulation units are added (see, for example, FIGS. 10, 12, 14 and 16 of Ohta). Therefore, Ohta does not disclose or suggest that the processor includes an authentication buffer configured to provide the ciphered data to the authentication cores each in an amount based on the corresponding authentication algorithm implemented.

New claim 30 is further distinguished from Ohto. Claim 30 recites that an authentication buffer is configured to receive unciphered data. Ohta only uses the data block accumulation unit 103 if the data is encrypted (See FIGS. 2 and 3 of Ohta).

Independent claims 10, 18, 25 and 32 include corresponding features to claim 1. Applicants submit that the Ohta reference should also be withdrawn with respect to claims 10, 18, 25 and 32 for at least the same reasons as claim 1.

For at least the foregoing reasons, Applicants request withdrawal of the art rejections.

Applicants submit that all dependent claims now depend on allowable independent claims.

It is believed that all of the pending claims have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for withdrawing the prior art cited with regards to any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as intent to concede any issue with regard to any claim, except as

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specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

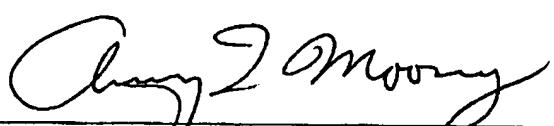
Applicants submit that the entire application is now in condition for allowance. Such action is respectfully requested at the Examiner's earliest convenience.

All correspondence should be directed to the address below. Applicants' attorney can be reached by telephone at (781) 401-9988 ext. 23.

No fee is believed to be due for this Response; however, if any fees are due, please apply such fees to Deposit Account No. 50-0845 referencing Attorney Docket: INTEL-013PUS.

Respectfully submitted,

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